

Attorney Docket No.: PATENT
SONY-15700



Attorney Docket No.: PATENT
SONY-15700

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	Group Art Unit: 2154
Richard A. Bardini et al.)	Examiner: Siddiqi, Mohammad A.
Serial No.: 09/920,660)	<u>SUPPLEMENTAL INFORMATION</u>
Filed: August 1, 2001)	<u>DISCLOSURE STATEMENT</u>
For: AN APPARATUS FOR AND)	162 N. Wolfe Road
METHOD OF INVALIDATING OR)	Sunnyvale, CA 94086
DELETING DIGITAL CONTENT)	(408) 530-9700
AFTER IT EXPIRES BY)	Customer No. 28960
COMPARING THE EMBEDDED)	
TIME WITH A GLOBAL TIME)	

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicants have become aware of the following printed publications which may be material to the examination of this application:

- "P1394 Standard For A High Performance Serial Bus," IEEE, 1995, pages 1-384.
- "The IEEE-1394 High Speed Serial Bus", R.H.J. Bloks, Philips Journal of Research, Vol 50., No. 1/2, pp. 209-216, 1996.
- "A Bus on a Diet-The Serial Bus Alternative. An Introduction to the 1394 High Performance Serial Bus", Michael Teener, CompCon92, February 1992, pp. 316-321.

This Supplemental Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: February 22, 2005

By: Jonathan O. Owens
Jonathan O. Owens
Reg. No.: 37,902

Attorneys for Applicants

STANDARD FORM NO. 100-10
This document is a copy of a document filed with the
United States Patent and Trademark Office (USPTO) and is
not to be construed as a representation that a search has been made,
that additional information material to the examination of this application
does not exist, or that anyone or more of these citations constitutes prior art.
Commissioner for Patents, P.O. Box 1650 Alexandria, VA
22313-1650

HAVERSTOCK & OWENS LLP

Date: 2-22-05 Jonathan O. Owens

FORM PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark Office

Attorney Docket No.: SONY-15700

Serial No.: 09/920,660

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use Several Sheets If Necessary)

Applicants: Richard A. Bardini et al.

(37 CFR § 1.98(b))

Filing Date: August 1, 2001

Group Art Unit: 2154

U.S. PATENT DOCUMENTS

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
	AK							
	AL							
	AM							
	AN							
	AQ							

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

	AP	"P1394 Standard For A High Performance Serial Bus," IEEE, 1995, pages 1-384.
	AQ	"The IEEE-1394 High Speed Serial Bus", R.H.J. Bloks, Philips Journal of Research, Vol 50., No 1/2, pp. 209-216, 1996.
	AR	"A Bus on a Diet-The Serial Bus Alternative. An Introduction to the 1394 High Performance Serial Bus", Michael Teener, CompCon92, February 1992, pp. 316-321.
	AS	
	AT	
	AU	
	AV	
	AW	
	AX	
	AY	
	AZ	

Examiner:

Date Considered:

EXAMINER:

Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.